

Profile

Lakshmikantha A C

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Objective:

To become a part of result oriented and highly successful team involved in implementing innovative ideas in true with quality standards.

Profile Summary

- Post Graduate in Electronics.
- One research paper presented at national conference
- Three years of teaching experience as a guest Faculty at University of Mysore Dept of Studies in Electronics, P.G centre, Hemangotri, Hassan.
- Effectively involved in Syllabus modification, Lab designing, Lab manual and study materials preparation of M.Sc. Electronics
- And also have an experience of guiding projects of B.Sc electronics students
- Good in these areas: Analog circuit design lab, Digital communication lab, Power electronics lab

Experience

Place of work: University of Mysore, Hemangotri PG centre, Hassan, Karnataka

Period - August 2014 to till date
Designation - Guest Lecturer
Nature of work - Teaching
Achievements - Coordinator in national level seminar RAOT 2016.

Academic Qualification

Sl. No	Course	Institution/University	Specialization	Year of Passing	Percentage of Marks
1	M.Sc	University of Mysore	Electronics	2010	59.15%
2	B.Sc	University of Mysore	PME	2008	56.47%
	B.Ed	University of Tumkur	PM	2015	65.50%
4	SSLC	Navodaya Junior College		2002	58.56%

Educational Summary:

M.Sc:

Specialization: Electronics

Project: *"The study of design and implementation of circular micro strip patch antenna (CMPA)"*

Achievements:

- Participation in Sports and Cultural events.
- Treasurer for student Association.

B.Sc:

Specialization: Physics, Mathematics and Electronics

Achievements:

- Participation in Sports and Cultural events.

B.Ed

Specialization: Physics, Mathematics

Achievements:

- Participation in Sports and Cultural events.

Paper presented in National conference:

- Presented a paper titled *“The design of two compact square micro strip patch antenna (SMPA) with DMS slotted ground plane for WLAN applications (NCET-2010) organized by Thiagarajar college of engineering, Madurai, Tamil Nadu.*

Basic Knowledge:

- Analog circuit design lab, Digital communication lab, Power electronics lab.
- HFSS (EM tool): Simulation and analysis of Micro strip Patch antenna (Circular and Rectangular)

Personal Details

Date of Birth	20 th SEPTEMBER 1986
Father Name:	Channakeshava A
Address	S/o Channakeshava A #354 ,Library Road. behind TP office Channarayapatna , Hassan , Karnataka-573116
Sex	Male.
Email id	kanta.acc@gmail.com
Contact No.	<u>9844534734</u>
Marital status	Married.
Nationality	Indian

Declaration

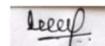
I hereby declare that the above furnished information's are true with respect to my knowledge

Thanking you

Date: 20-04-2021

You're faithfully

Place: Hassan



(LAKSHMIKANTHA A C)